



Patent 1449A/PTO		Complete If Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		Application Number	10/074,732
		Filing Date	2/13/2002
		First Named Inventor	Leo Mathew et al.
		Gro-up Art Unit	2815
		Examiner Name	Paul E. Brock II Drew
Sheet 1 of 1	Attorney Docket Number	SC11805TP	

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	CA	US - 6,330,184	12-11-2001	White et al.	
	CB	US - 6,097,065	08-01-2000	Forbes et al.	
	CC	US - 6,150,687	11-21-2000	Noble et al.	
	CD	US - 6,011,726	01-04-2000	Eltan	
	CE	US - 5,804,848	09-08-1998	Mukai	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>2</sup> Number <sup>4</sup> Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>3</sup>
	CF	DE 101 25 967 C1	05-29-2001	Schlösser et al.		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	CG	CHAN et al.; "A True Single-Transistor Oxide-Nitride-Oxide EEPROM Device"; IEEE Electron Device Letters; March 1987; pp 93-95; Vol. EDL-8, No. 3	
	CH	GONZALES SR.; "A Dynamic Source-Drain Extension (DSDE) MOSFET using a Separately Biased Conductive Spacer"; Semiconductor Device Research Symposium International; December 5-7, 2001; pp. 645-648; IEEE.	
Examiner Signature			Date Considered 2/16/05

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